Docket No.: 4943-105 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Devries, C. et al.

Serial No.: Herewith Group Art Unit: TBD

Filed: July 28, 2003 Examiner: TBD

Title: POWER DOWN SYSTEM AND METHOD FOR INTEGRATED

CIRCUITS

Information Disclosure Statement

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, Applicants hereby bring to the attention of the United States Patent and Trademark Office the following documents of which Applicants have been made aware. The documents are listed on the attached PTO Form 1449. Copies of the documents are enclosed.

Applicants submit that this Information Disclosure Statement is submitted within six months of the filing date.

It is requested that the references be considered by the Examiner and be made of record as part of the available prior art under 37 C.F.R. § 1.104. It is believed that no fee is required. The

Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 13-2165.

Respectfully submitted,

Diane Dunn McKay

Reg. No. 34,586

Attorney for Applicant(s)

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.	SERIAL NO.			
4943-105 US	Herewith			
Devries, C. et al.				
Devries, C. et al. FILING	GROUP			

			541, 20, 2000				
		U.S	S. PATENT DOCUMENTS				
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	5,058,203	10/15/1991	Inagami				
	5,142,699	08/25/1992	Sato et al.				
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	DOGGNEN NOMBER					YES	NO
	OTHER DOCUME	NTS (Includir	ng Author, Title, Date, Pertinent	Pages, Etc.)			
EXAMINER			DATE CONSIDERED				
*EXAMINER: Ir	nitial if reference considered, wheth	er or not citation is	in conformance with MPEP 609; Draw	line through cit	ation if not in c	onformance a	ind not

Form PTO-A820 (also form PTO-1449)

considered. Include copy of this form with next communication to applicant.

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				Devries, C. et al.					
				FILING July 28, 200		GROUP TBD			
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	OTHER DOCUM	MENTS (Includir	ng Authoi	r, Title, Date, Pertinen	t Pages, Etc.)			
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